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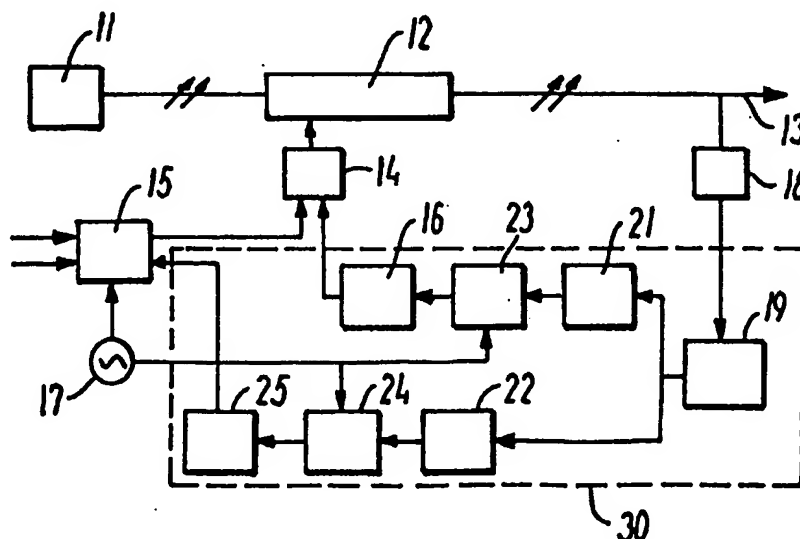
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(54) Title: A METHOD AND AN APPARATUS FOR MODULATING LIGHT IN A MODULATOR CIRCUIT COMPRISING A MACH-ZEHNDER MODULATOR



(57) Abstract

The invention relates to a method and a modulator circuit for modulating light in a modulator circuit. The modulator circuit comprises a Mach-Zehnder modulator (12) having at least one optical input and at least one optical output, said Mach-Zehnder modulator (12) modulating light, which is fed to the input of the Mach-Zehnder modulator (12) in response to applied electrical modulation signals from a driver circuit (15). According to a method of the invention, the applied modulation signal on the electrodes of the Mach-Zehnder modulator is superimposed by a pilot tone which is thus superimposed by the modulated light emitted by the Mach-Zehnder modulator. In response to the emitted modulated light, an electrical feedback circuit (30) generates bias control signals for the bias circuit (16) and driver control signals for the driver circuit (15) for regulating the bias signal and the driver signal fed to the Mach-Zehnder modulator (12).

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A method and an apparatus for modulating light in a modulator circuit comprising a Mach-Zehnder modulator

5 Background art

The invention relates to a method of modulating light as defined in the introductory portions of claims 1 and 5, and a modulator circuit as defined in the introductory  
10 portions of claims 14 and 15.

Optical communication involves the need of modulating the optical signals.

15 This modulation may be performed by direct modulation of the light source, e.g. by direct modulation of a laser diode with data signals, but it is preferred in several connections to perform modulation on the light itself. This type of modulation is called optical modulation.

20

A problem involved by the direct optical modulation is that the characteristics of the modulator change over time as a function of temperature, changed properties of the material, etc. This change specifically means that  
25 the optical output as a function of the electrical driver signals is not constant, which results in undesired distortion of the optical signal.

US Patent Specification No. 5 170 274 describes an optical transmitter of the Mach-Zehnder type, in which feedback from the modulator is obtained by means of a low-frequency pilot tone superimposed on the electrical driver signal of the modulator, thereby providing some compensation for the drift of the operating point of the  
30 modulator. However, this transmitter has the drawback that the extinction ratio is gradually impaired because  
35

of amplitude drift of the modulated signal, or more particularly a drift with respect to the amplitude of the transmission characteristic of the Mach Zehnder modulator.

5

#### Summary of the invention

When, as stated in claim 1, the photoelectrical converter circuit is connected to and generates a feedback signal  
10 for a feedback circuit which is also connected to the pilot tone generator, said feedback circuit being adapted, in response to the pilot tone signal fed from the pilot tone generator and the feedback signal fed from the photoelectrical converter circuit, to generate driver control signals for the driver circuit to regulate the  
15 driver signal fed to the Mach-Zehnder modulator, it is possible to achieve a very well-defined regulation of a modulator circuit. Thus, the invention enables regulation, optimum in some cases, of the fed driver signal,  
20 i.e. the modulation amplitude, which is fed to the Mach-Zehnder modulator. The regulation may thus be performed automatically in connection with temperature errors or fluctuations.

25 This may be a particular advantage in connection with e.g. so-called bias-free Mach-Zehnder modulators, where bias corrections should be avoided, since such bias corrections will cause drift in the overall circuitry.

30 In case of drift or e.g. temperature fluctuations, it is thus possible according to the invention to control the amplitude of the modulation signal.

It should be noted that it is not decisive according to  
35 the invention how the modulation signals, formed by the driver signal and the pilot tone, are fed to the Mach-

Zehnder modulator. The pilot tone and the driver signal may e.g. be mixed electrically before the signal is fed to the electrodes of the modulator, and the driver signal and the pilot tone may be fed to separate electrodes or  
5 sets of electrodes in the Mach-Zehnder modulator, if desired.

When, as stated in claim 2, the pilot tone signal is fed to the logic "0" and/or logic "1" of the modulation signal,  
10 and the feedback circuit provides driver control signals in response to the pilot tone component of the feedback signal corresponding to the pilot tone signal, a particularly advantageous embodiment of the invention is obtained, as the regulation of the modulation voltage is  
15 "tuned" to either the ideal logic "0" or the ideal logic "1". The amplitude regulation of the modulation voltage may thus be adjusted to a desired asymmetric proportion without bias regulation, which is extremely advantageous in several applications, since regulation of the bias/DC  
20 operating point per se results in electrode drift.

Regulation of the amplitude thus makes it possible to currently allow for e.g. temperature-caused changes in the transmission characteristic which particularly concern the actual amplitude of the transmission characteristic.  
25

Thus, the regulation according to the invention may thus allow for the undesirability of applying a DC voltage to the Mach-Zehnder modulator, as this DC voltage gives rise to drift in the transmission characteristic.  
30

When, as stated in claim 3, the driver signal, in response to the pilot tone signal fed to the modulation signal, is regulated to the logic "0" or logic "1" of the modulation signal in accordance with either  
35

$$\Delta V_{\text{amp}} \sim F_L \cdot k_1 \quad \text{or}$$

$$\Delta V_{\text{amp}} \sim F_H \cdot k_2$$

5 where  $\Delta V_{\text{amp}}$  is the change in the driver signal provided by  
the regulation,  $k_1$  and  $k_2$  are a suitably selected con-  
stant for the algorithm,  $F_L$  and  $F_H$  assume the value +1  
when the pilot tone component at logic "0" and logic "1",  
respectively, is in in-phase with the corresponding pilot  
10 tone generated by the pilot tone generator, and  $F_L$  and  $F_H$   
assume the value -1 when the pilot tone component at  
logic "0" and logic "1", respectively, is in antiphase  
with the corresponding pilot tone generated by the pilot  
tone generator, a very efficient regulation algorithm is  
15 achieved according to the invention, as the feedback  
circuit hooks on to either the logic "0" or the logic "1"  
and the corresponding pilot tone. It should be stressed  
that actually it is immaterial whether pilot tones are  
generated at both logic "0" and logic "1", provided it is  
20 a pilot tone reference corresponding to the one of the  
above-mentioned algorithms which has now been selected.

The amplitude regulation of the modulation signal thus  
gives an improved extinction ratio, i.e. the ratio of  
25 logic "1" ( $p_1$ ) to logic "0" ( $p_0$ ) is increased, and the  
resulting signal/noise ratio in a complete application  
may therefore be increased. It should be noted in this  
connection that this improved extinction ratio is  
achieved, even though the amplitude regulation provides  
30 an asymmetric modulation of the transmission characteris-  
tic, as one extreme of the amplitude is adjusted to the  
ideal logic "1" or logic "0", respectively, without the  
opposite logic level being optimized.

35 Usually, it is preferred to regulate to logic "0", and  
thus according to the algorithm  $\Delta V_{\text{amp}} \sim F_L \cdot k_1$ .

As appears from the above-mentioned algorithm, the regulation is proportional to  $k$  and  $F$ , which likewise means that a special case may be that  $\sim$  is replaced by  $=$ .

- 5 When, as stated in claim 4, the driver signal, in response to the pilot tone signal fed to the modulation signal, is regulated to the logic "0" or logic "1" of the modulation signal in accordance with either

10 
$$\Delta V_{\text{amp}} \sim F_L \cdot k_1 |A_L| \text{ or}$$

$$\Delta V_{\text{amp}} \sim F_H \cdot k_2 |A_H|$$

where  $|A_L|$  or  $|A_H|$  is the numeric amplitude of the pilot  
15 tone components detected in the feedback circuit at logic "0" and logic "1", respectively, it is additionally made possible to weight the regulation algorithm according to the actual distance from the modulation point to the desired optimum modulation point corresponding to either  
20 logic "1" or logic "0". This may be an advantage e.g. when starting the system at modulation amplitude zero, as the algorithm tracks the desired modulation point more rapidly.

- 25 When, as stated in claim 5, the photoelectrical converter circuit is connected to and generates a feedback signal for a feedback circuit which is also connected to the pilot tone generator, said feedback circuit being adapted, in response to the pilot tone signal fed from the pilot  
30 tone generator and the feedback signal fed from the photoelectrical converter circuit, to generate bias control signals for the bias circuit and driver control signals for the driver circuit to regulate the bias signal and driver signal fed to the Mach-Zehnder modulator, it is  
35 possible to perform a combined regulation of a modulator

circuit by means of both regulation of the system bias (or DC operating point) and the modulation amplitude.

It is noted that it is not decisive according to the invention how the modulation signals, formed by the driver signal, the bias signal and the pilot tone, are fed to the Mach-Zehnder modulator. The bias signal may e.g. be mixed electrically with the driver signal before the driver signal is fed to the electrodes of the Mach-Zehnder modulator, and the bias signal, the driver signal and optionally the pilot tone may be fed to separate electrodes or sets of electrodes in the Mach-Zehnder modulator, if desired.

When, as stated in claim 6, the pilot tone signal is fed with the same amplitude to the logic "0" and logic "1" of the modulation signal, but with opposite phase, it is ensured that the complete system may be dimensioned very uniformly.

20

However, it should be noted that according to the teaching of the invention, in contrast to known regulation techniques, it is not decisive whether the two pilot tones used have the same phase, frequency, curve shape or amplitude, which may be extremely advantageous in several connections, since it is just necessary to have a pilot tone reference with which the pilot tone component or components fed back are compared.

When, as stated in claim 7, the feedback circuit detects the pilot tone content of the feedback signal for the logic "1" level and the logic "0" level, respectively, and determines the phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator for both the logic "1" level and the logic "0" level, it is possible to obtain a simple and optimum



modulation of the transmission characteristic of the Mach-Zehnder modulator with simultaneous regulation of modulation amplitude and bias. Detection at both the logic "0" level and the logic "1" level thus ensures continuous retention or regulation in accordance with the ideal or almost ideal modulation levels, said phase additionally giving clear information as to whether modulation is performed on increasing or decreasing half-periods of the transmission characteristic.

10

When, as stated in claim 8, the feedback circuit generates a bias control signal for the bias circuit and a driver control signal for the driver circuit to achieve a state of equilibrium in which the phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator for the both the logic "1" level and the logic "0" level is the same, a simple regulation application is achieved, it being possible beforehand to specify in the regulation algorithm whether phase-locked modulation on the increasing or decreasing half-periods of the transmission characteristic is desired.

15  
20

It will be appreciated that said regulation algorithm may be supplemented with additional regulation parameters or strategies, it being possible e.g. to introduce a compensation particularly for the bias drift over time. Thus, according to certain guidelines, it may thus be decided to reduce the bias voltage by jumping one period or half a period down the V axis of the transmission characteristic.

25  
30

Further, situations may occur in which it would be advantageous to change the sign of the bias voltage according to e.g. a mean value consideration of modulation data over a period of time.

35

When, as stated in claim 9, the feed back circuit generates a bias control signal for the bias circuit and a driver control signal for the driver circuit to maintain a phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator for both the logic "1" level and the logic "0" level with the same sign, a very simple and clear application is achieved, as the feedback signal at logic "0" and logic "1" is specifically used for regulating the driver circuit or the bias circuit, respectively, or vice versa.

Thus, it is noted that the ratio of the phase of the pilot tone component from the pilot tone generator to the phase of the corresponding, detected pilot tone component may have an opposite sign, if this applies to the pilot tone components at logic high as well as logic low. This means that the bias signal puts a DC operating point on the decreasing part of the transmission characteristic.

The decisive point according to the invention is thus to ensure that at logic high and at logic low, respectively, the pilot tone component has the same sign with respect to the added pilot tone, depending on whether the regulation is performed as a function of logic low or logic high. Thus, the regulation of the transient exclusively depends on the selected regulation parameter or parameters.

When, as stated in claim 10, the bias control signal is provided with a rapid time constant and the drive control signal with a slow time constant, or vice versa, a practical regulation algorithm is achieved, capable of hooking on to an optimum or stable state in a predictable manner.

When, as stated in claim 11, the bias control signal and the driver control signal are provided sequentially, an alternative embodiment of the invention is achieved, likewise capable of reproducibly hooking on to an optimum and stable state.

When, as stated in claim 12, the amplitude of the pilot tone content in the feedback signal for the logic high level and the logic low level as well as the associated phase ratio of the corresponding pilot tones regulates the bias circuit and the driver circuit or the driver circuit and the bias circuit, respectively, in accordance with the regulation algorithms

$$\Delta V_{\text{bias}} \sim -F_L \cdot k_1 \cdot |A_L|$$

$$\Delta V_{\text{amp}} \sim F_H \cdot k_2 \cdot |A_H|$$

where  $\Delta V_{\text{bias}}$  is the change in the bias voltage provided by the regulation,  $\Delta V_{\text{amp}}$  is the change in the driver signal provided by the regulation,  $k_1$  and  $k_2$  are a suitably selected constant for the algorithm, and  $F_L$  and  $F_H$  assume the value +1 when the pilot tone component at logic "0" and logic "1", respectively, is in in-phase with the corresponding pilot tone generated by the pilot tone generator, and  $F_L$  and  $F_H$  assume the value -1 when the pilot tone component at logic "0" and logic "1", respectively, is in antiphase with the corresponding pilot tone generated by the pilot tone generator, and  $|A_L|$  and  $|A_H|$  are the numeric amplitudes of the pilot tone components detected in the feedback circuit at logic "0" and logic "1", respectively, a compact and efficient regulation of the modulator circuit is achieved.

It is noted that a special case in accordance with the regulation algorithm is that  $|A_L|$  and  $|A_H|$  are set at 1,

which means in practice that the amplitudes are not included in the algorithms.

A further special case is that "~" is replaced by "=",  
5 which makes the algorithm extremely simple and efficient.

All the last-mentioned algorithms are surprisingly simple, as the determined reference point of logic "0" and logic "1", respectively, individually controls bias  
10 and amplitude, respectively.

When, as stated in claim 13, the amplitude of the pilot tone content of the feedback signal for the logic high level and the logic low level as well as the associated  
15 phase ratio of the corresponding pilot tones regulates the bias circuit and the driver circuit or the driver circuit and the bias circuit, respectively, in accordance with the regulation algorithms

$$20 \quad \Delta V_{\text{amp}} \sim F_L \cdot k_1 \cdot |A_L|$$

$$\Delta V_{\text{bias}} \sim -F_H \cdot k_2 \cdot |A_H|$$

where  $\Delta V_{\text{bias}}$  is the change in the bias voltage provided by  
25 the regulation,  $\Delta V_{\text{amp}}$  is the change in the driver signal provided by the regulation,  $k_1$  and  $k_2$  are a suitably selected constant for the algorithm,  $F_L$  and  $F_H$  assume the value +1 when the pilot tone component at logic "0" and logic "1", respectively, is in in-phase with the corresponding pilot tone generated by the pilot tone generator,  
30 and  $F_L$  and  $F_H$  assume the value -1 when the pilot tone component at logic "0" and logic "1", respectively, are in antiphase with the corresponding pilot tone generated by the pilot tone generator, and  $|A_L|$  and  $|A_H|$  are the  
35 numeric amplitudes of the pilot tone components detected in the feedback circuit at logic "0" and logic "1", re-

spectively, a corresponding simple algorithm is achieved, as, in this case, the amplitude regulation is locked directly to logic "0" and the bias is locked directly to logic "1".

5

When, as stated in claim 14, the photoelectrical converter circuit is connected to a feedback circuit which is also connected to the pilot tone generator, said feedback circuit being adapted to generate driver control  
10 signals for the driver circuit connected therewith in response to detected pilot tone components in the feedback signal, an implementation of the above-mentioned method is achieved for a modulator circuit with amplitude regulation.

15

When, as stated in claim 15, the photoelectrical converter circuit is connected to a feedback circuit which is also connected to the pilot tone generator, said feedback circuit being adapted to generate bias control signals for the bias circuit connected therewith and driver  
20 control signals for the driver circuit connected therewith in response to detected pilot tone components in the feedback signal, an implementation of the above-mentioned method is achieved for a modulator circuit with combined  
25 bias and amplitude regulation.

The invention will be described below with reference to the drawing, in which

30 fig. 1 shows an explanatory sketch of a simple Mach-Zehnder modulator,

fig. 2 shows the transmission characteristic of a Mach-Zehnder modulator,

35

fig. 3 shows an explanatory diagram of an embodiment of the invention,

fig. 4 shows the envelope of the superimposed pilot tone,

5

fig. 5 shows the envelope of the superimposed pilot tone in case of drift on bias, and

fig. 6 illustrates the mode of operation of the circuit  
10 shown in fig. 5.

#### Example

Fig. 1 shows an explanatory sketch of a simple Mach-Zehnder modulator. The light may be modulated in a Mach-Zehnder transmitter by suitable mixing of two light signals with a certain mutual phase shift, said modulator having an input end 1 and an output end 2.  
15

20 The input end 1 of the modulator is split into two optical branches 3 and 4 and are joined again for the optical output end 2 of the modulator.

In the example shown, a constant light source (not shown)  
25 can thus feed light to the input end 1 and then emit modulated light at the output end 2.

A pair of electrodes 5 is connected to one optical branch 4 so that a signal applied to the electrode 5 causes application of a corresponding electrical field on the optical branch 4, which in turn results in changed optical transmission properties on the branch 4 with respect to the propagation velocity of the light.  
30

35 In the case shown, the mutual phase shift is achieved by splitting light from a light source in two optical con-

ductors 3 and 4, an electrical field being applied to one optical conductor 4 by means of an electrode 5. This electrical field gives rise to a phase delay on the optical branch 4 in dependence on the electrical field applied, and then the resulting light output is modulated in dependence on the mutual phase ratio of the light in the two optical branches 3 and 4 on the output 2 of the modulator.

Thus, a laser source may be modulated, as needed, on the optical output by regulating the mutual phase shift between the two optical branches 3 and 4.

A Mach-Zehnder modulator is a well-known component.

The electrodes may be configured in several different ways. For example, separate pairs of electrodes may be used on each optical branch 3 and 4.

Fig. 2 shows the transmission characteristic for a given ideal Mach-Zehnder modulator, where the V axis indicates the applied electrode voltage, while  $T(V)$  indicates the transmission characteristic of the Mach-Zehnder modulator as a function of the applied electrode voltage V.

The ideal output of the Mach-Zehnder modulator of the type concerned is given by the transmission characteristic

$$T(V) = \cos^2 \left( \phi_0/2 - \pi V/2V_\pi \right)$$

where  $\phi_0$  is a constant depending on the difference in distance between the optical branches 3, 4,  $V_\pi$  is a material-dependent constant, and V is the applied electrode voltage. As appears from fig. 2, this transmission characteristic is periodic. Transmission of light may thus be

modulated dynamically in response to the applied electrode voltage. As illustrated by the curve, a DC signal  $V_d$  gives rise to an operating point B on the transmission characteristic. A superimposed AC voltage on the electrode, which operates between  $V_1$  and  $V_2$ , is thus included in the transmission characteristic in the points B1 and B2.

In connection with modulation of data signals between high and low intensity levels it is desirable owing to the signal/noise ratio that the distance between the upper and lower points B1 and B is as great as possible, thereby providing the greatest possible modulation of the complete system. It will also be seen from fig. 2 that, ideally, the operating point should be located at  $T(V) = \frac{1}{2}$ , and that the amplitude of the AC component of the electrode signal should be in the vicinity of  $V\pi$ , so that B1 and B2 should be modulated as close as possible on A and C, respectively.

20

However, a practical problem in this connection is that the transmission characteristic drifts because of the temperature conditions and the structure of the electrodes. This means that the operating point, if no countermeasures are taken, drifts away from the point B, causing the resulting output signal on the Mach-Zehnder modulator output to be distorted correspondingly.

In addition to the drift with respect to the synchronization of the operating point B, the ratio of the maximum of  $T(V)$  to the maximum of  $V\pi$  will likewise drift, causing the modulation of the AC component to be impaired.

Fig. 3 shows an explanatory diagram of an embodiment of the invention.



A DFB laser 11 is optically connected to a Mach-Zehnder modulator 12 which feeds an optical output 13.

With respect to the overall mode of operation, the shown  
5 modulator 12 corresponds in principle to the Mach-Zehnder modulator shown in fig. 1, as, in this case, the modulator output 2 corresponds to the optical output 13.

The Mach-Zehnder modulator 12 receives an electrical  
10 driver signal for the electrodes from a so-called Bias TEE 14, which is in turn connected to a driver 15 and a bias control 16.

Thus, the Mach-Zehnder modulator 12 is controlled by the  
15 driver circuit 15 which emits a signal voltage in response to the applied data signal DATA.

The shown Bias TEE 14 serves to mix the DC operating point B on the transmission characteristic  $T(V)$  shown in  
20 fig. 2 with an AC modulation signal  $V\pi$ . The operating point should be as close as possible to B, and any deviation from B should be up the curve toward B1 rather than down the curve owing the resulting signal/noise ratio. According to the invention, the modulation signal itself  
25 will be modulated such that, at the extremes, the superimposed modulation is as close to A or C as possible.

The electrical data input signal is fed to the Bias TEE 14 from the driver 15, as mentioned above, in response to  
30 the modulated data signals, while the operating point is fed from the bias control 16.

The modulator circuit of the invention additionally comprises a pilot tone generator 17 electrically connected  
35 to the driver 15 so that the driver signal generated by the data signal is superimposed by a pilot tone.

The typical ratio of the driver signal frequency to the pilot tone frequency is that the driver signal has a high frequency, while the pilot tone has a relatively low frequency. However, it should be stressed that according to the invention it is not decisive that the driver signal has a higher frequency than the pilot tone. The decisive point is that there is a certain numeric difference between driver signal and pilot tone.

10 The basic modulation of the driver signal is 2.5 Gbits/s and that of the pilot tone is 1.8 kHz according to the present embodiment. Other modulation and pilot tone frequencies may be selected.

15 The amplitude of the pilot tone signal should be relatively low with respect to the driver signal.

The transmission system of the invention is additionally provided with a feedback circuit 30 which is fed back to the bias control 16 and the driver 15 from the optical output 13 of the Mach-Zehnder modulator.

Part of the optical output signal from the Mach-Zehnder modulator is thus branched to an O/E converter 18 which generates an electrical feedback signal in response to the branched optical feedback signal.

The electrical feedback signal is fed further on to a peak-peak detector 19, which is electrically connected to two detectors, a zero detector 21 and a mark detector 22, respectively.

Each of the detectors 21 and 22 is moreover electrically connected to the input of a phase detector 23 and the input of a phase detector 24, respectively. In practice,

the phase detectors 23 and 24 will be formed by a sample-and-hold circuit.

5 The outputs of the phase detectors 23 and 24 are fed to the bias control 16 and a driver control circuit 25, respectively.

10 The peak-peak detector 19, the zero detector 21, the mark detector 22, the phase detectors 23 and 24, the bias control 16 and the driver control circuit 25 together form the feedback circuit 30 shown in dashed line.

The inputs of the phase detectors 23 and 24 are also electrically connected to the pilot tone generator 17.

15

The peak-peak detector 19 serves to currently detect the electrical minimum and the electrical maximum, which are subsequently fed to the zero detector 21 and the mark detector 22, respectively. The two output signals from the  
20 peak-peak detector 19 will thus form the envelopes of the signals representing a logic low and the signals representing a logic high, respectively.

25 These two envelopes represent the pilot tone components at the points B1 and B2 in the transmission characteristic shown in fig. 2.

In practice, the peak-peak detector 19 is dimensioned to provide a sufficient number of peak detections to give a  
30 satisfactory quality of the resulting envelopes.

In principle, in the resulting signal on the transmission characteristic, the envelopes will independently approach zero for the logic low envelope when B1 moves toward A,  
35 and approach zero for the logic high envelope when B2 approaches the point C.

It is noted for explanatory reasons that the envelopes are not necessarily of the same numeric size, if the bias point drifts.

- 5 If, because of overload and/or drift of the working point, the signal  $V\pi$  gets beyond the minimum A of the transmission characteristic, it is noted that the pilot tone signal generated by the pilot tone generator 17 and the resulting pilot tone signal registered by the zero  
10 detector will be in antiphase. The same applies to overload beyond the maximum C of the transmission characteristic.

- Subsequently, the phase detectors 23 and 24, controlled  
15 by the pilot tone generator 17, will register the amplitude of the envelopes of the zero detector 21 and the mark detector 22, respectively, and register the phases of these. This may be done in practice e.g. in that the maximum and the minimum of the pilot tone trigger sam-  
20 pling on the envelopes.

As stated above, the outputs of the phase detectors 23 and 24 are fed further on to the bias control 16 and a driver control circuit 25, respectively.

25

The driver control circuit 25 is moreover electrically connected to the driver 15, and the bias circuit 16 is electrically connected to the Bias TEE 14.

- 30 The driver control circuit 25 will thus regulate the modulation level, i.e. the AC amplitude of the control voltage V which is fed to the driver 15 and thereby to the Mach-Zehnder modulator 12.

Thus, via the Bias TEE 14, the bias control 16 regulates the introduction of bias, i.e. the DC operating point, on the Mach-Zehnder modulator 12.

- 5 The mode of operation of the above-mentioned modulator will be elaborated below with reference to fig. 4 and fig. 5.

Fig. 4 shows the fundamental development of an ideal  
10 modulation of a Mach-Zehnder modulator according to the invention, where the T axis indicates the transmission characteristic or optical output as a function of the modulation voltage  $v$  on the V axis.

- 15 It will thus be seen that the modulation voltage to logic zero is  $V_0$ , and the modulation voltage to logic high is  $V_1$ .

The modulation of the driver signal between  $V_0$  and  $V_1$   
20 will proceed with a relatively high frequency, and accordingly just the envelope of the modulation signal will be considered for this purpose.

In the case shown, logic high  $V_1$  and logic low  $V_0$  are  
25 both superimposed by a pilot tone. The cycle time of the pilot tone is  $1/f_0$ , where  $f_0$  indicates the frequency of the pilot tone. For explanatory reasons, the pilot tones are shown with considerably higher amplitudes than those occurring in practice.

30

As will be seen, the superimposed pilot tones of logic low and logic high, respectively, on the V axis are the same in this case, but with opposite phase.

At the time  $t_1$ , the superimposed pilot tone on logic low thus has a minimum, while the pilot tone, likewise at the time  $t_1$ , has a minimum with respect to the V axis.

5 It also appears from the figure that the pilot tones superimposed on the modulation voltage give rise to superimposed low-frequency pilot tones in the resulting light, it being noted that the resulting isolated pilot tones have the same phase as the applied pilot voltage. A minimum of the pilot tone superimposed on logic zero at the  
10 time  $t_1$  on the V axis causes a minimum in the transmission characteristic or the optical output at the time  $t_1$ . Correspondingly, the shown maximum of the pilot tone superimposed on logic 1 at the time  $t_1$  on the V axis gives  
15 a maximum in the transmission characteristic or the optical output at the time  $t_1$ .

In the shown example, both the superimposed pilot tones on the V axis thus oscillate in the same phase as the resulting pilot tone light.  
20

Fig. 5 illustrates how the invention handles a beginning error state in the form of drift of the operating point.

25 It should be stressed that the shown conditions between pilot tones and modulation signals are not to scale. It should moreover be stressed that the occurred error state is not to scale either, as the shown error situation is corrected currently.

30

It will thus be seen from fig. 5 that the pilot tones, still superimposed on logic "0" and logic "1", respectively, with opposite sign, now give rise to changed pilot tone components in the output signal.

35

In the case shown, as a consequence of a drift the bias voltage is thus no longer correct, and the modulation of the electrodes on the Mach-Zehnder modulator (not shown) on the V axis therefore does not give an optimum modulation in accordance with the transmission characteristic  $T(V)$ .

The level of the emitted light at logic high has thus decreased, and the level of the optical signal at logic low has increased, which causes i.a. an impaired signal/noise ratio of the modulated light on the output of the Mach-Zehnder modulator.

The drift of the modulator, specifically the electrodes of the modulator, means that the pilot tone component for logic high still oscillates in phase with the corresponding pilot tone on the output of the modulator, it being also seen that the pilot tone superimposed on logic low now oscillates in the opposite phase to the corresponding pilot tone on the output.

Thus, at the time  $t_1$ , the pilot tones on the output signal both have a maximum.

In addition, the amplitude of the superimposed pilot tones in the output signal has increased (which is not readily apparent from the drawings). The reason why the amplitude increases is that the inclination of the  $T(V)$  characteristic increases toward its maximum when the modulation voltage changes from the maxima and minima of  $T(V)$ .

According to the invention, the modulation circuit shown in fig. 3 will detect that the phase has changed between the modulation signal on the input and output of the modulator for logic low, just as the modulation circuit

will detect an increasing amplitude independently for the pilot tone at logic low and logic high.

If the shown drift of the operating point had likewise  
5 been superimposed by a drift on the amplitude ratio of  $V$  to  $T(V)$  and thus had given rise to an asymmetric drift, the feedback circuit would likewise independently have detected the two (and different) amplitudes for logic high and logic low, respectively.

10

Subsequently, the feedback signal will provide control signals for the bias circuit 16 and the driver circuit 15 whereby the operating point on the  $V$  axis, i.e. the bias voltage, is adjusted to achieve a symmetric modulation of  
15  $T(V)$ , following which the amplitude of the driver signal applied to the Mach-Zehnder modulator 12 is as low as possible at both logic high and logic low, so that the transmission characteristic  $T(V)$  is modulated optimally.

20 Conversely, the feedback circuit may initially regulate the driver circuit 15 and thereby the amplitude of the modulation signal on the  $V$  axis to achieve uniform amplitudes at logic high as well as at logic low while maintaining opposite phase, and then the operating point is  
25 adjusted until the phases of the pilot tone signals on the output again correspond to the phases on the pilot tones, as shown in fig. 4.

However, it should be noted that depending on the selected regulation algorithm it may also be decided to adjust the bias so that, after the regulation, the operating point is present on the downwardly extending part of the transmission characteristic, for which reason, according to the invention, the pilot tone components on  
35 the output may be in opposite phase to the corresponding



applied pilot tones on condition that both pilot tone components are in opposite phase.

In practice, the bias circuit 16 as well as the driver  
5 circuit may be regulated at the same time with different time constants on the feedback control signals.

Fig. 6 shows an explanatory diagram of a further embodiment of the invention.

10

A DFB laser 41 is optically connected to a Mach-Zehnder modulator 42, which feeds an optical output 43.

With respect to the overall mode of operation, the shown  
15 modulator 42 corresponds in principle to the Mach-Zehnder modulator of fig. 1, but, according to the present embodiment, the modulator is of the bias-free type. Thus, no bias voltage is to be fed to the modulator 42, which thus has a minimum drift with respect to the zero crossing  
20 of the transmission characteristic.

The Mach-Zehnder modulator 42 receives an electrical driver signal for the electrodes from the driver 45.

25 The Mach-Zehnder modulator 42 is thus controlled by the driver circuit 45, which emits a signal voltage in response to the applied data signal DATA.

The modulation circuit additionally comprises a pilot  
30 tone generator 47 which is electrically connected to the driver 45 so that the driver signal generated by the data signal is superimposed by a pilot tone.

According to this embodiment, the pilot tone will exclu-  
35 sively be modulated on the logic 0's of the modulator.

The transmission system of the invention is additionally provided with a feedback which is fed back to the driver 45 from the optical output 43 of the Mach-Zehnder modulator.

5

Part of the optical output signal from the Mach-Zehnder modulator is thus branched to an O/E converter 48 which generates an electrical feedback signal in response to the branched optical feedback signal.

10

The electrical feedback signal is fed further on to a peak-peak detector 49 which is moreover electrically connected to a zero detector 52.

15

The detector 52 is moreover electrically connected to the input of a phase detector 54. In practice, the phase detector 54 will be formed by a sample-and-hold circuit.

20

The input of the phase detector 54 is likewise electrically coupled to the pilot tone generator 47.

25

The peak-peak detector 49 serves to currently detect the electrical minimum and the electrical maximum which are subsequently fed to the zero detector 52. Output signals from the peak-peak detector 49 will thus form the envelopes for the signals which represent a logic low.

30

In this embodiment, too, the peak-peak detector 49 is detected such that a sufficient number of peak detections are achieved to give a satisfactory quality of the resulting envelopes.

35

In the resulting signal on the transmission characteristic, the envelopes will, in principle independently, approach zero for the logic low envelope curve when B1

moves toward A, and approach zero for the logic high envelope when B2 approaches the point C.

Subsequently, the phase detector 54, controlled by the pilot tone generator 47, will register the phase of the envelopes from the zero detector 52. This may be done in practice e.g. in that the maximum and the minimum of the pilot tone trigger sampling on the envelopes.

The output of the phase detector 54 is fed further on to a driver control circuit 55.

The driver control circuit 55 is moreover electrically connected to the driver 45.

The driver control circuit 55 will thus regulate the modulation level, i.e. the AC amplitude of the control voltage V which is fed to the driver 45 and thereby to the Mach-Zehnder modulator 42.

In this case, the driver control circuit 55 provides a change in the amplitude gain  $\Delta V_{\text{amp}}$  in accordance with the algorithm

$$\Delta V_{\text{amp}} = F_L \cdot k_1$$

where  $k_1$  is a suitably selected constant for the algorithm, and  $F_L$  assumes the value +1 when the pilot tone component at logic "0" is in in-phase with the corresponding pilot tone generated by the pilot tone generator, and  $F_L$  assumes the value -1 when the pilot tone component at logic "0" is in antiphase with the corresponding pilot tone generated by the pilot tone generator.

The algorithm may optionally be modified to

$$\Delta V_{\text{amp}} = F_L \cdot k_1 |A_L|$$

where the amplitude  $|A_L|$  of the pilot tone component registered in the feedback signal provides a regulation  
5 which is additionally dependent on the actual distance of the logic "0" from the ideal zero of the transmission characteristic, where  $|A_L|$  ideally assume the value 0.

A further modification of the above-mentioned circuit may  
10 e.g. be to feed the pilot tone directly to a separate pilot tone electrode set in the Mach-Zehnder modulator.

Thus, according to the algorithm shown above, the resulting amplitude regulation of the modulation signal, i.e.  
15 the driver signal, is locked to the ideal logic zero on the transmission characteristic.

Finally, it should be noted that it is not decisive according to the invention whether the regulation functions  
20 are performed exactly in the stated and well-defined circuits, as it is the overall functionality of the circuits which provides the invention.

## P a t e n t   C l a i m s :"

1. A method of modulating light in a modulator circuit comprising a Mach-Zehnder modulator (12) having at least one optical input and at least one optical output, said  
5 Mach-Zehnder modulator (12) modulating light, which is fed to the input of the Mach-Zehnder modulator (12), in response to applied electrical modulation signals, said modulator circuit additionally comprising
- 10 - a driver circuit (15) for generating a driver signal with a logic "1" and a logic "0" in response to a data signal,
- 15 - a pilot tone generator (17) for generating a pilot tone having a frequency essentially different from the frequency with which the Mach-Zehnder modulator (12) modulates, said modulation signals being formed by the driver signal and the pilot tone, and
- 20 - a photoelectrical converter circuit for providing an electrical feedback signal in response to the optical output signal of the Mach-Zehnder modulator,
- 25 c h a r a c t e r i z e d in that the photoelectrical converter circuit (18) is connected to and generates a feedback signal for a feedback circuit (30) which is also connected to the pilot tone generator (17), said feedback circuit (30) being adapted, in response to the pilot tone
- 30 signal fed from the pilot tone generator (17) and the feedback signal fed from the photoelectrical converter circuit (18), to generate driver control signals for the driver circuit (15) to regulate the driver signal fed to the Mach-Zehnder modulator (12).
- 35
2. A method according to claim 1, c h a r a c t e r -

i z e d in that the pilot tone signal is fed to the logic "0" and/or logic "1" of the modulation signal, said feedback circuit providing driver control signals in response to the pilot tone component of the feedback signal  
5 corresponding to the pilot tone signal.

3. A method according to claim 1 or 2, c h a r a c -  
t e r i z e d in that the driver signal, in response to the pilot tone signal fed to the modulation signal, is  
10 regulated to the logic "0" or logic "1" of the modulation signal in accordance with either

$$\Delta V_{\text{amp}} \sim F_L \cdot k_1 \quad \text{or}$$

15 
$$\Delta V_{\text{amp}} \sim F_H \cdot k_2$$

where  $\Delta V_{\text{amp}}$  is the change in the driver signal provided by the regulation,  $k_1$  and  $k_2$  are a suitably selected constant for the algorithm,  $F_L$  and  $F_H$  assume the value +1  
20 when the pilot tone component at logic "0" and logic "1", respectively, is in in-phase with the corresponding pilot tone generated by the pilot tone generator, and  $F_L$  and  $F_H$  assume the value -1 when the pilot tone component at logic "0" and logic "1", respectively, is in antiphase  
25 with the corresponding pilot tone generated by the pilot tone generator.

4. A method according to claims 1-3, c h a r a c t e r -  
i z e d in that the driver signal, in response to the  
30 pilot tone signal fed to the modulation signal, is regulated to the logic "0" or logic "1" of the modulation signal in accordance with either

$$\Delta V_{\text{amp}} \sim F_L \cdot k_1 |A_L| \quad \text{or}$$

35

$$\Delta V_{\text{amp}} \sim F_H \cdot k_2 |A_H|$$

where  $|A_L|$  or  $|A_H|$  is the numeric amplitude of the pilot tone components detected in the feedback signal at logic "0" and logic "1", respectively.

- 5 5. A method of modulating light in a modulator circuit comprising a Mach-Zehnder modulator (12) having at least one optical input and at least one optical output, said Mach-Zehnder modulator (12) modulating light, which is fed to the input of the Mach-Zehnder modulator (12), in  
10 response to applied electrical modulation signals, said modulator circuit additionally comprising
- a driver circuit (15) for generating a driver signal with a logic "1" and a logic "0" in response to a data  
15 signal,
  - a bias circuit (16) for generating a bias signal,
  - a pilot tone generator (17) for generating one or more  
20 pilot tones each having a frequency essentially different from the frequency with which the Mach-Zehnder modulator (12) modulates, said modulation signals being formed by the driver signal, the bias signal and the pilot tone, and
  - a photoelectrical converter circuit for providing an  
25 electrical feedback signal in response to the optical output signal of the Mach-Zehnder modulator,
- 30 c h a r a c t e r i z e d in that the photoelectrical converter circuit (18) is connected to and generates a feedback signal for a feedback circuit (30) which is also connected to the pilot tone generator (17), said feedback circuit being adapted, in response to the pilot tone sig-  
35 nal fed from the pilot tone generator (17) and the feedback signal fed from the photoelectrical converter cir-

cuit (18), to generate bias control signals for the bias circuit (16) and driver control signals for the driver circuit (15) to regulate the bias signal and the driver signal fed to the Mach-Zehnder modulator (12)).

5

6. A method according to claims 1-5, characterized in that the pilot tone signal is fed with the same amplitude to the logic "0" and logic "1" of the modulation signal, with opposite phase.

10

7. A method according to claims 1-6, characterized in that the feedback circuit (30) detects the pilot tone content of the feedback signal for the logic "1" level and the logic "0" level, respectively, and determines the phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator (17) for both the logic "1" level and the logic "0" level.

20

8. A method according to claims 1-7, characterized in that the feedback circuit (30) generates a bias control signal for the bias circuit (16) and a driver control signal for the driver circuit (15) to achieve a state of equilibrium in which the phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator (17) for both the logic "1" level and the logic "0" level is the same.

25

9. A method according to claims 1-8, characterized in that the feedback signal (30) generates a bias control signal for the bias circuit (16) and a driver control signal for the driver circuit (15) to maintain a phase between said pilot tone content and the pilot tone or tones generated by the pilot tone generator (17) for both the logic "1" level and the logic "0" level with the same sign.

35



10. A method according to claims 1-9, c h a r a c -  
t e r i z e d in that the bias control signal is pro-  
vided with a rapid time constant, and the driver control  
signal has a slow time constant, or vice versa.

5

11. A method according to claims 1-10, c h a r a c -  
t e r i z e d in that the bias control signal and the  
driver control signal are provided sequentially.

10

12. A method according to claims 1-11, c h a r a c -  
t e r i z e d in that the amplitude of the pilot tone  
content of the feedback signal for the logic high level  
and the logic low level as well as the associated phase  
ratio of the corresponding pilot tones regulate the bias  
circuit (16) and the driver circuit (15) or the driver  
circuit (15) and the bias circuit (16), respectively, in  
accordance with the regulation algorithms

20

$$\Delta V_{bias} \sim -F_L \cdot k_1 \cdot |A_L|$$

$$\Delta V_{amp} \sim F_H \cdot k_2 \cdot |A_H|$$

25

where  $\Delta V_{bias}$  is the change in the bias voltage provided by  
the regulation,  $\Delta V_{amp}$  is the change in the driver signal  
provided by the regulation,  $k_1$  and  $k_2$  are a suitably se-  
lected constant for the algorithm,

30

$F_L$  and  $F_H$  assume the value +1 when the pilot tone compo-  
nent at logic "0" and logic "1", respectively, is in in-  
phase with the corresponding pilot tone generated by the  
pilot tone generator, and  $F_L$  and  $F_H$  assume the value -1  
when the pilot tone component at logic "0" and logic "1",  
respectively, is in antiphase with the corresponding pi-  
lot tone generated by the pilot tone generator, and  $|A_L|$   
and  $|A_H|$  are the numeric amplitudes of the pilot tone

35

components detected in the feedback circuit at logic "0" and logic "1", respectively.

13. A method according to claims 1-12, characterized in that the amplitude of the pilot tone content of the feedback signal for the logic high level and the logic low level as well as the associated phase ratio of the corresponding pilot tones regulates the bias circuit (16) and the driver circuit (15) or the driver circuit (15) and the bias circuit (16), respectively, in accordance with the regulation algorithms

$$\Delta V_{\text{amp}} \sim F_L \cdot k_1 \cdot |A_L|$$

$$\Delta V_{\text{bias}} \sim -F_H \cdot k_2 \cdot |A_H|$$

where  $\Delta V_{\text{bias}}$  is the change in the bias voltage provided by the regulation,  $\Delta V_{\text{amp}}$  is the change in the driver signal provided by the regulation,  $k_1$  and  $k_2$  are a suitably selected constant for the algorithm,  $F_L$  and  $F_H$  assume the value +1 when the pilot tone component at logic "0" and logic "1", respectively, is in in-phase with the corresponding pilot tone generated by the pilot tone generator, and  $F_L$  and  $F_H$  assume the value -1 when the pilot tone component at logic "0" and logic "1", respectively, is in antiphase with the corresponding pilot tone generated by the pilot tone generator, and  $|A_L|$  and  $|A_H|$  are the numeric amplitudes of the pilot tone components detected in the feedback circuit at logic "0" and logic "1", respectively.

14. A modulator circuit for performing the method according to claims 1-4, comprising a Mach-Zehnder modulator (12) having at least one optical input and at least one optical output, said Mach-Zehnder modulator (12) modulating light, which is fed to the input of the Mach-

Zehnder modulator (12), in response to applied electrical modulation signals, said modulator circuit additionally comprising

5 - a driver circuit for generating a driver signal in response to a data signal,

- a pilot tone generator for generating a pilot tone having a frequency essentially different from the frequency  
10 with which the Mach-Zehnder modulator modulates, said modulation signal being formed by the driver signal and the pilot tone, said modulator circuit additionally comprising

15 - a photoelectrical converter circuit for providing an electrical feedback signal in response to the optical output signal of the Mach-Zehnder modulator,

c h a r a c t e r i z e d in that the photoelectrical  
20 converter circuit (18) is connected to a feedback circuit (30) which is also connected to the pilot tone generator (17), said feedback circuit (30) being adapted to generate driver control signals for the driver circuit (15) connected therewith in response to detected pilot tone  
25 components in the feedback signal.

15. A modulator circuit for performing the method of claims 5-13, comprising a Mach-Zehnder modulator (12) having an optical input and an optical output, said Mach-  
30 Zehnder modulator (12) modulating light, which is fed to the input of the Mach-Zehnder modulator (12), in response to applied electrical modulation signals, said modulator circuit additionally comprising

35 - a driver circuit for generating a driver signal in response to a data signal,

- a bias circuit for generating a bias signal,
  - a pilot tone generator for generating a pilot tone having a frequency essentially different from the frequency with which the Mach-Zehnder modulator modulates, said modulation signals being formed by the driver signal, the bias signal and the pilot tone, said modulator circuit additionally comprising
  - 10 - a photoelectrical converter circuit for providing an electrical feedback signal in response to the optical output signal of the Mach-Zehnder modulator,
- c h a r a c t e r i z e d in that the photoelectrical
- 15 converter circuit (18) is connected to a feedback circuit (30) which is also connected to the pilot tone generator (17), said feedback circuit (30) being adapted to generate bias control signals for the bias circuit (16) connected therewith and driver control signals for the
- 20 driver circuit (15) connected therewith in response to detected pilot tone components in the feedback signal.

1/3

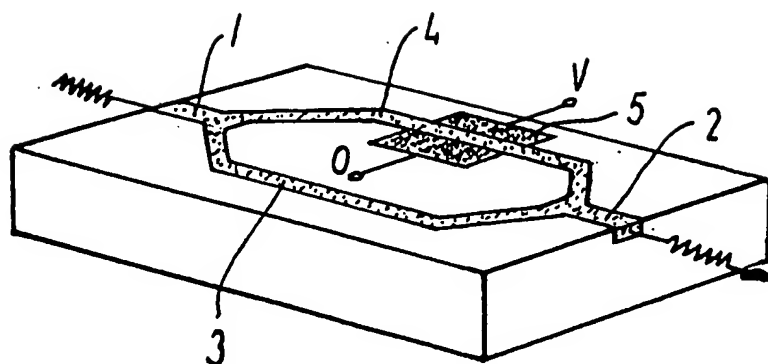


FIG. 1

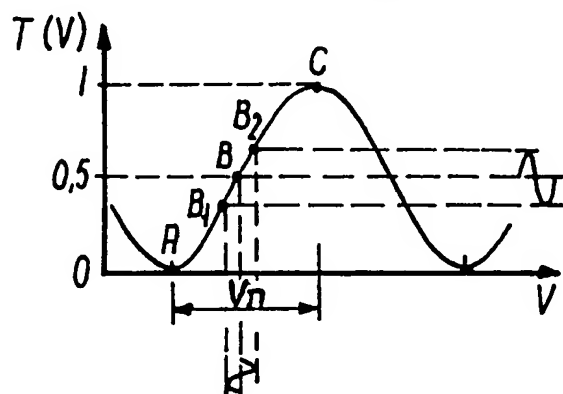


FIG. 2

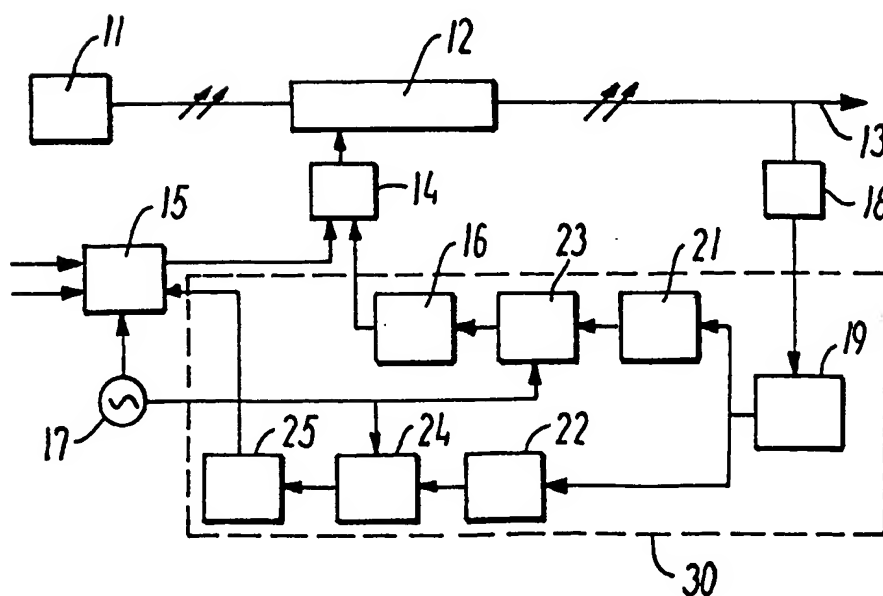
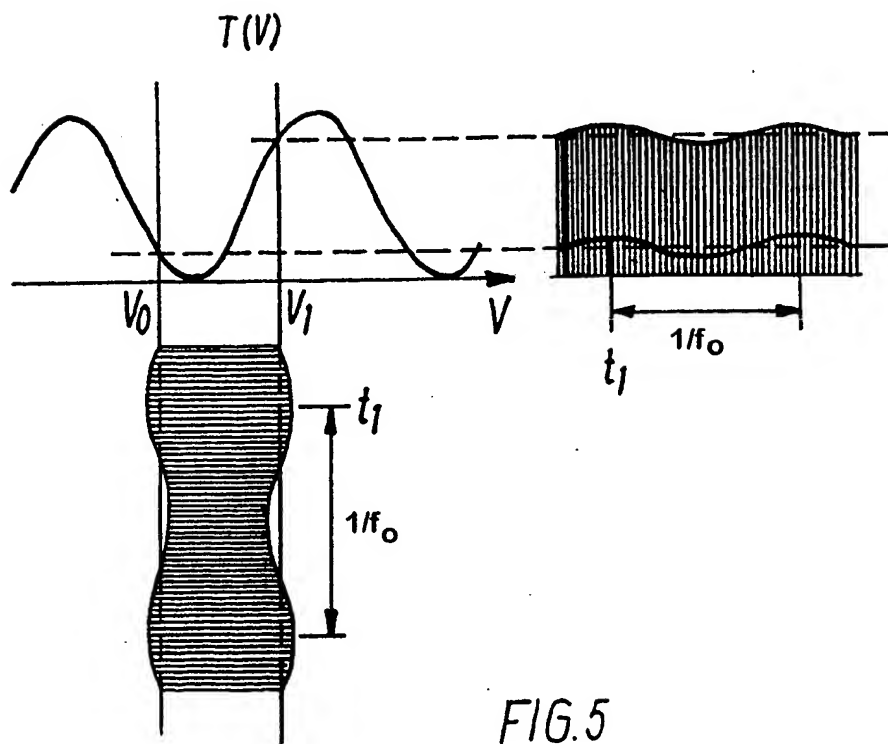
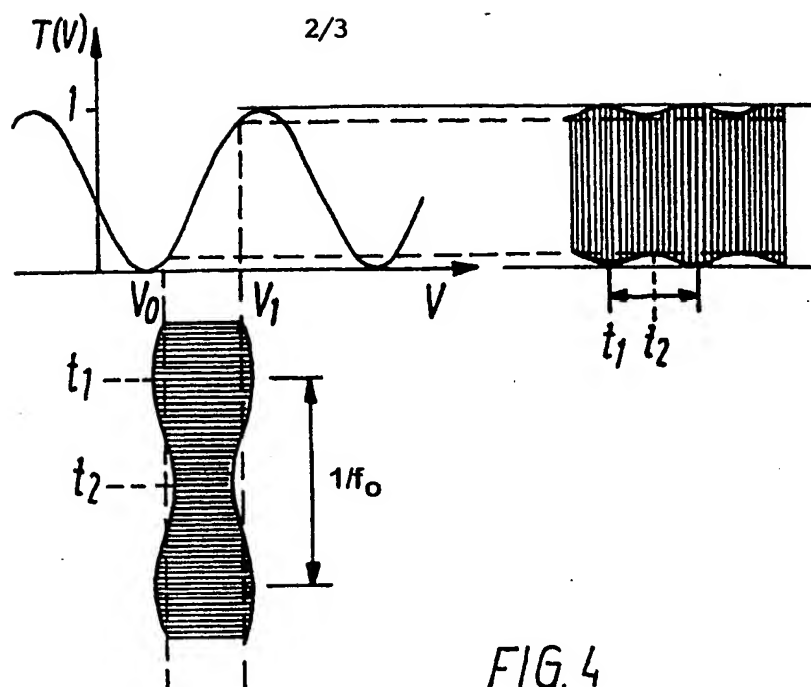


FIG. 3



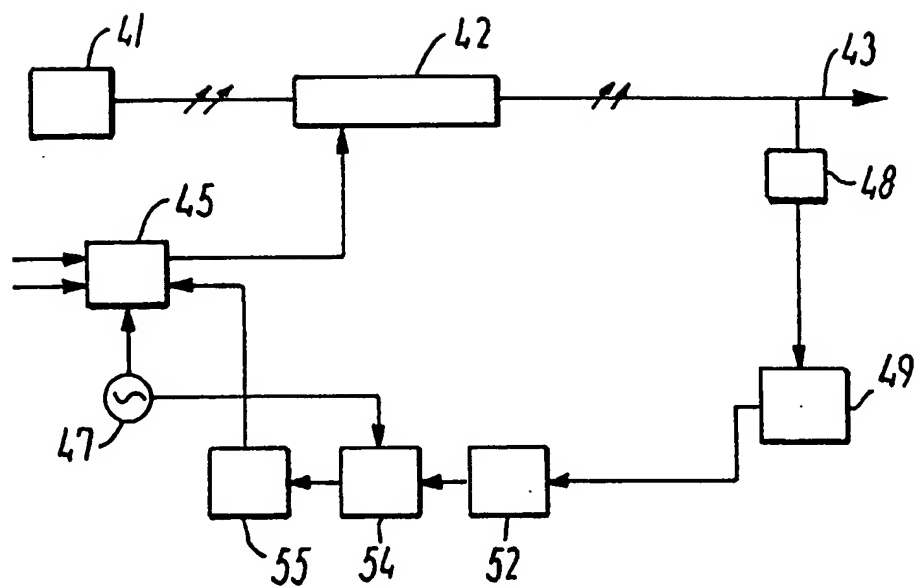


FIG. 6

## INTERNATIONAL SEARCH REPORT

International application No.

PCT/DK 97/00590

## A. CLASSIFICATION OF SUBJECT MATTER

IPC6: G02F 2/00, H04B 10/04

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: G02F, H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

## WPI, CLAIMS

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5170274 A (N. KUWATA ET AL.), 8 December 1992 (08.12.92), column 4, line 21 - line 60, figures 1, 2, abstract --	1-15
A	US 5515199 A (J. FARINA), 7 May 1996 (07.05.96), column 8, line 17 - line 64, figure 8, abstract --	1-15
A	US 5003624 A (W.H. TERBRACK ET AL), 26 March 1991 (26.03.91), abstract -- -----	1-15

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

## \* Special categories of cited documents:

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16 April 1998

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**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

02/04/98

International application No.  
PCT/DK 97/00590

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5170274 A	08/12/92	CA 2037351 A,C DE 69127840 D,T EP 0444688 A,B JP 2642499 B JP 3251815 A JP 4140712 A	02/09/91 05/03/98 04/09/91 20/08/97 11/11/91 14/05/92
US 5515199 A	07/05/96	AU 4706796 A CA 2211923 A EP 0807340 A IL 116954 D WO 9624201 A	21/08/96 08/08/96 19/11/97 00/00/00 08/08/96
US 5003624 A	26/03/91	EP 0448832 A JP 4253023 A	02/10/91 08/09/92